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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/763,270	01/26/2004	Takuya Kotsuji	016907-1596	6450
22428 7590 05/14/2007 FOLEY AND LARDNER LLP SUITE 500 3000 K STREET NW WASHINGTON, DC 20007			EXAMINER LEE, JOHN W	
			ART UNIT 2624	PAPER NUMBER
			MAIL DATE 05/14/2007	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

<p align="center"><b>Office Action Summary</b></p>	<b>Application No.</b> 10/763,270	<b>Applicant(s)</b> KOTSUJI, TAKUYA	
	<b>Examiner</b> John Wahnkyo Lee	<b>Art Unit</b> 2624	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 26 January 2004.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-6 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-6 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 24 March 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948)   | 5) <input type="checkbox"/> Notice of Informal Patent Application                       |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date <u>20040126</u> . | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Information Disclosure Statement***

1. An initialed and dated copy of Applicant's IDS form from 1449, Paper No. 20040126, is attached to the instant Office action.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-2 and 5 are rejected under 35 U.S.C. 102(b) as being anticipated by Shikakura et al. (US 5,717,705).

Regarding claim 1, Shikakura discloses an encoding (Fig. 1-401) and a decoding device (Fig. 1-402) that is connected via a transmission path (FIG. 1-403). The transmission path can be dislike medium such as a floppy disk or optical disk, or a solid-state medium such as a semiconductor memory (col. 5, lines 34-36). The encoding device comprises an orthogonal transform circuit outputted as a low frequency and a high frequency component to be encoded in separate encoding circuits (Fig. 1-408 and 409) and a synthesis circuit (Fig. 1-412; col. 5, lines 43-60). The encoded data has a format that separates the components from each other in one synch block (Fig. 2; col. 6, lines 7-10) that is added by a synch signal addition circuit (Fig. 1-413) for the purpose of

Art Unit: 2624

transmission or recording (col. 5, lines 61-65). The encoded and synthesized data can be transmitted via a transmission path to the decoder device (Fig. 1) to be outputted to the output terminal (Fig. 1-110) connected to a monitor, printer, or the like for forming an image (col. 6, lines 56-61).

Regarding claim 2, Shikakura discloses an encoding device comprising an orthogonal transform circuit outputted as a low frequency and a high frequency component such as DC component and AC component separately (Fig. 1; col. 5, lines 46-50).

Regarding claim 5, Shikakura discloses a block formation circuit (Fig. 1-406) that divides data into units of blocks (col. 5; lines 40-43).

### ***Claim Rejections - 35 USC § 103***

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 3-4 and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shikakura et al. (US 5,717,705) in view of Matsumoto et al. (US 6,377,580).

Regarding claims 3, Shikakura discloses all the previous claim limitations except the detail claim limitation of claim 2. However, Matsumoto discloses a scanner (Fig. 1-111) which is composed of a Contact Sensor, an original feed mechanism and the like

optically reads an original image to separate it into Red, Green and Blue, RGB components (col. 3, lines 50-60).

It would have been *prima facie* obvious to one of ordinary skill in the art at the time the invention was made to use Matsumoto's apparatus in Shikakura's image processing apparatus to improve efficiency as suggested by Matsumoto (col. 1, lines 55-56) and to provide a system that is compatible with commonly used image processing data.

Regarding claim 4, Matsumoto further discloses Cyan, Magenta, Yellow, and Black components as an input (col. 4, lines 3-12).

Regarding claim 6, Regarding claim 6, Shikakura further discloses a transmission path that is in the form of a transmission medium for above-ground electric-wave or light spaces, such as an optical fiber, satellite or microwave guide, in the case of instantaneous transmission, and in the form of a storage medium including a tape-like medium such as a digital VTR or DAT, a disk-like medium such as a floppy disk or optical disk, and a solid state medium such as a semiconductor memory (col. 5, lines 28-35). Matsumoto discloses a CPU (Fig. 1-101) as a system control unit that controls the apparatus entirely (col. 2, lines 55-56).

### **Conclusion**

6. No claims are allowed.
7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to John Wahnkyo Lee whose telephone number is (571)

Art Unit: 2624

272-9554. The examiner can normally be reached on Monday - Friday (Alt.) 7:30 a.m. - 5:00 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jingge Wu can be reached on (571) 272-7429. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

John W. Lee  
(AU 2624)



JINGGE WU  
SUPERVISORY PATENT EXAMINER